

Grid Connected Converter

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Fahad Alghareeb
Di Miao
Kaiqiong Ji

Introduction

Client

Dr. Venkata Yaramasu
Assistant Professor at NAU
Director of AMPERE Lab
School of Informatics, Computing, and Cyber
Systems

Mentor

Ashwija Reddy Korenda

Client's Research Focus:

- A- Wind Power Systems.
- B- Photovoltaic Systems.
- C- Motor Drives.

Problem

- There is no laboratory scale prototype converters available in the market that fits all of these applications.
- Needs plug and play converter to fit the three power conversion applications.
- Having such converter will help our client to develop new power converter topologies and test new controlling schemes such as model predictive control.

Goal

- Implement and build a prototype of multilevel converter for high power applications and test it using model predictive control.
- Practical power level is at 5 MW.
- Prototype power level is at 5 kW.



Update WBS of Fahad

ID	Activity/Task	Description	Deliverables	Other People
Converter Designing				
1.0	Designing the NPC converter	Finding the parameters of the NPC converter using theoretical equations and according to the specification	Complete design of NPC converter	Sayaf & Mohammed
Hardware Implementation				
2.1	NPC converter hardware implementation	Implementing the designed NPC converter schematic on Hardware using PCB	Designed PCB	Sayaf & Mohammed
2.2	Components Soldering	Soldering all the components on a PCB	Complete NPC converter	Sayaf & Mohammed
Testing				
3.1	Testing the designed NPC converter	Check the working of the designed NPC converter	Testing results	Team
3.2	Testing the NPC converter with grid load	Check the working of the designed NPC converter with grid load	Testing results	Team
Documentation				
4.1	Documenting all the results	Storing all the results of the NPC converter for different load	Document containing all the results	Team
4.2	Writing IEEE paper	Writing the Simulations results and experimental results	IEEE paper	Sayaf
4.3	Revise & edit	Revise and edit the IEEE paper	Results Section	Team

1- Converter Designing : (DONE) 100%

- Hardware team have finished Designing the NPC converter.
- Checked the working of the designed NPC converter.

2- Hardware Implementation: (DONE) 100%

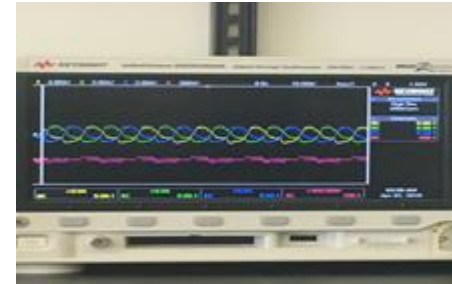
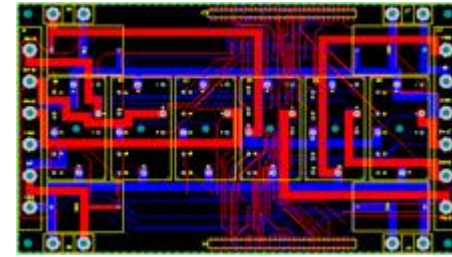
- Hardware Team have implemented the NPC converter schematic on hardware using PCB.

3- Testing: (DONE) 60% ← Current Status

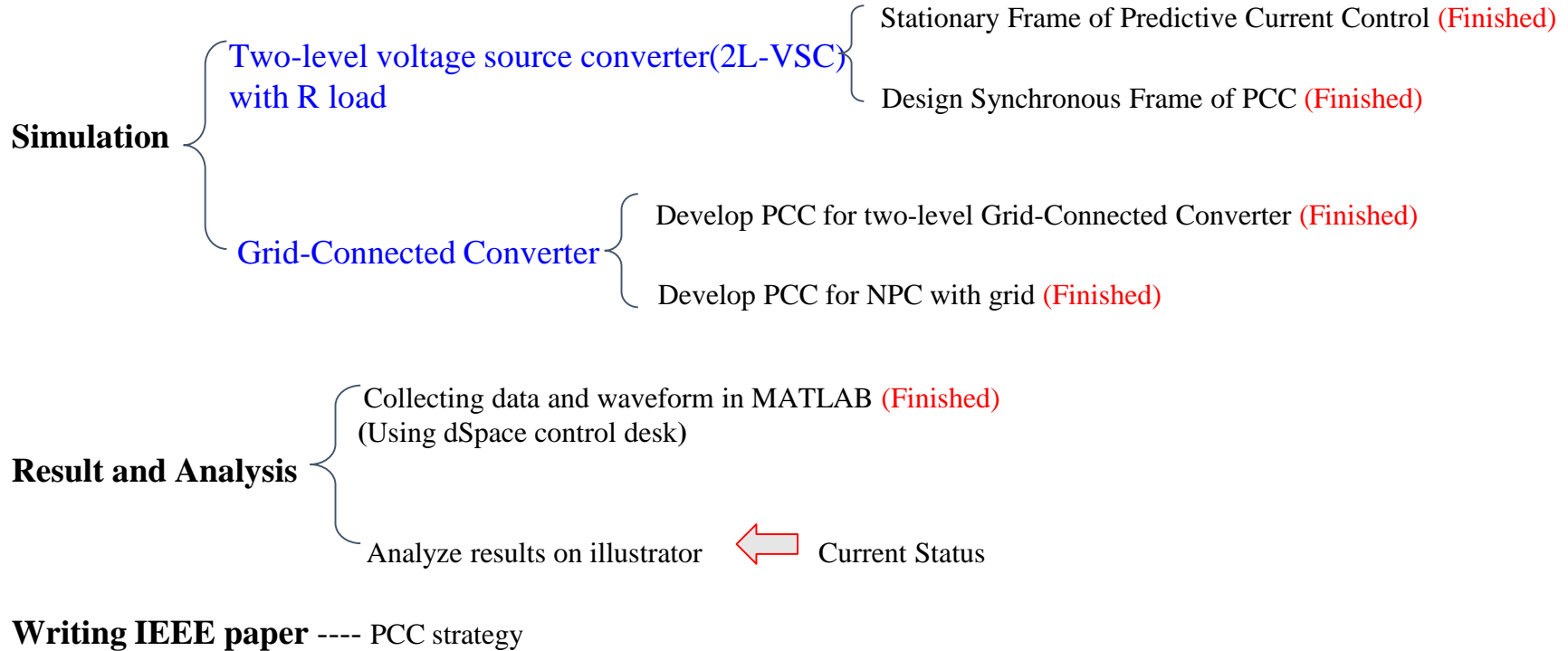
- Team Tested the designed NPC converter, But we faced problems in the converter.
- One of the gate drivers did not working.
- Replaced the gate driver with a new one.

4- Documentation:

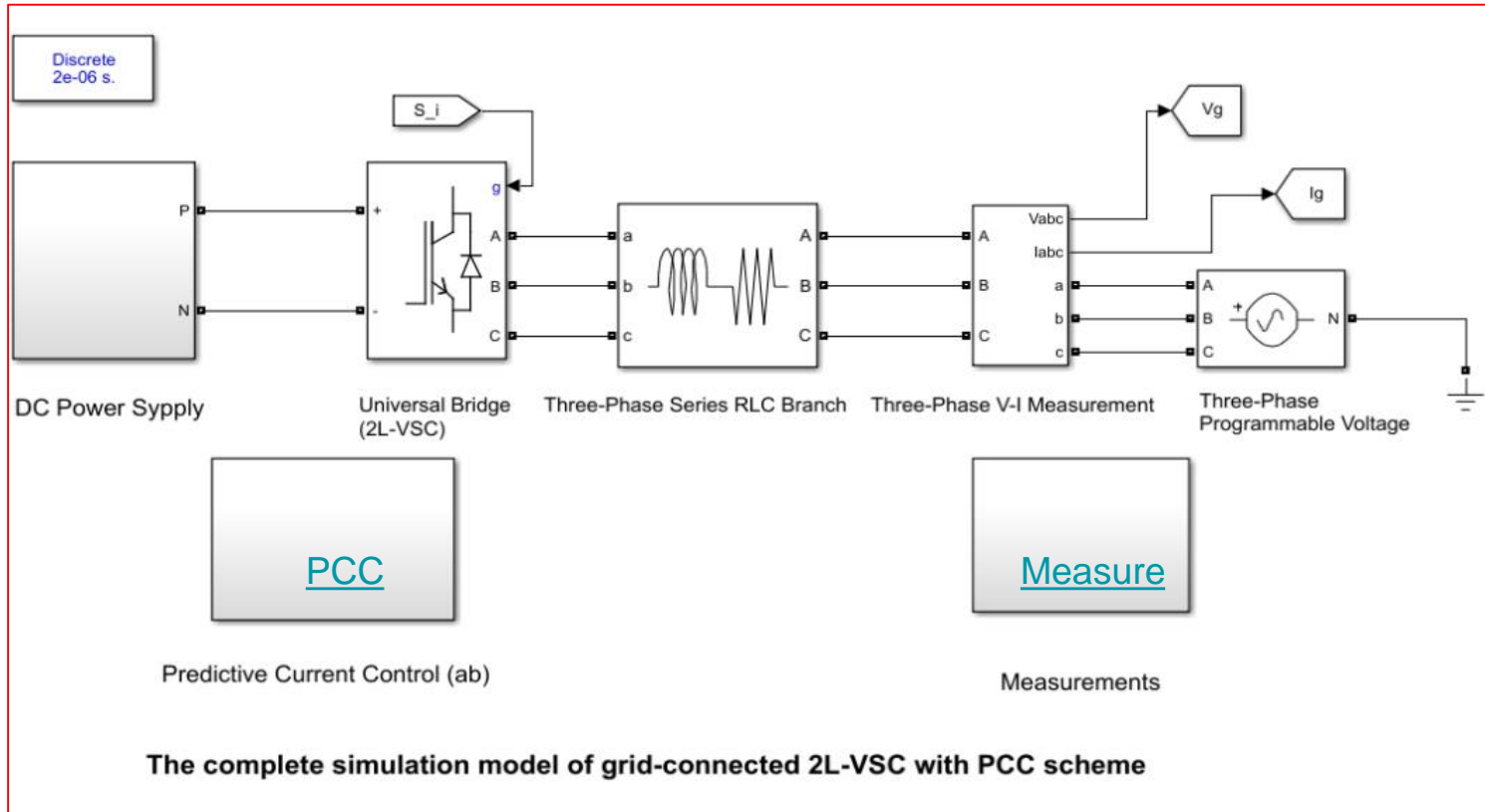
- Storing all the results of the NPC converter.
- Writing IEEE paper
- Revise and edit the IEEE paper



WBS of Kaiqiong Ji

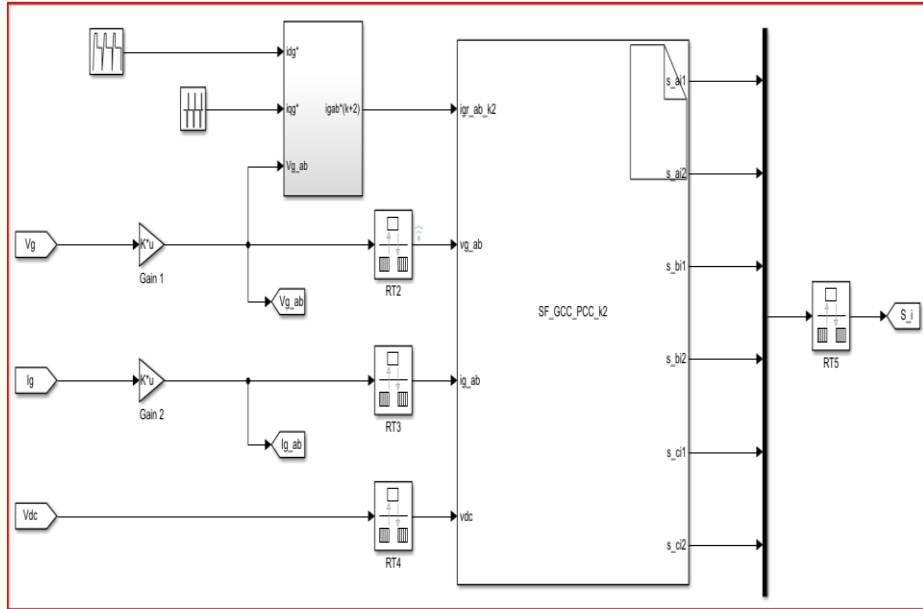


PCC for Two-level Grid-Connected Converter

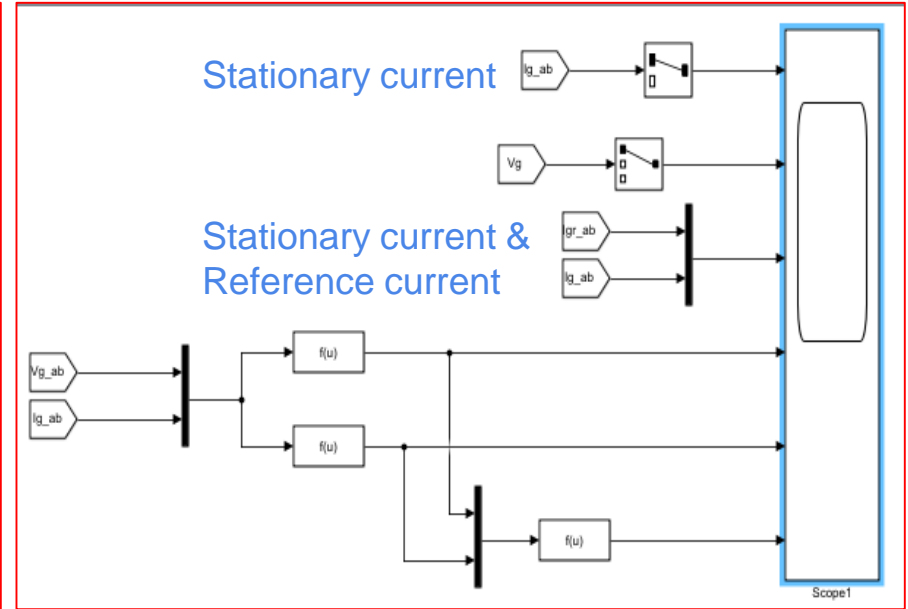


Subsystems of simulation

Predictive current control($\alpha\beta$ frame)



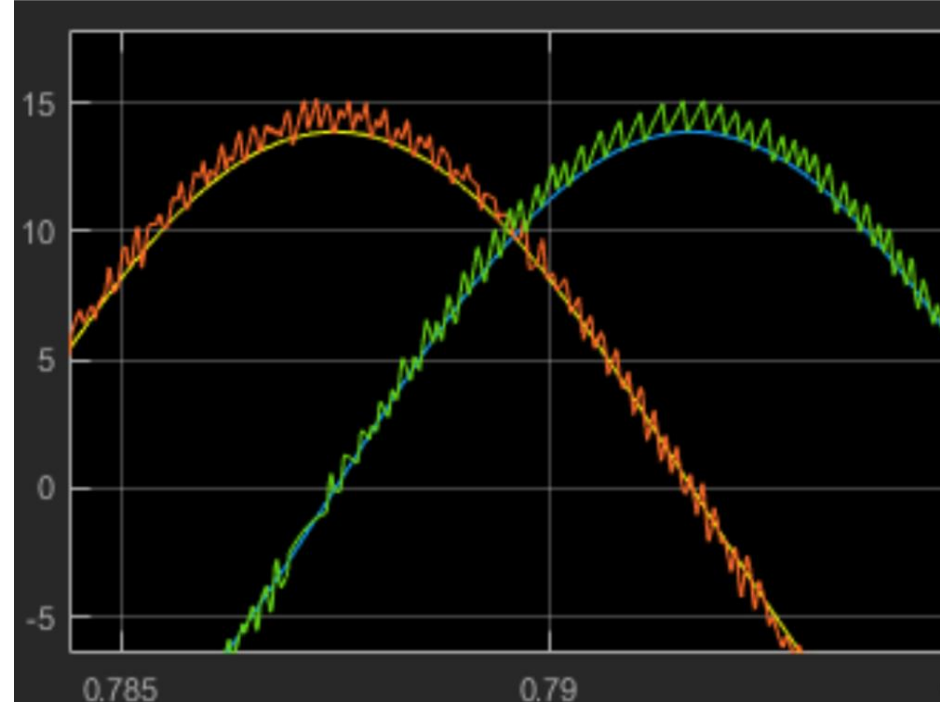
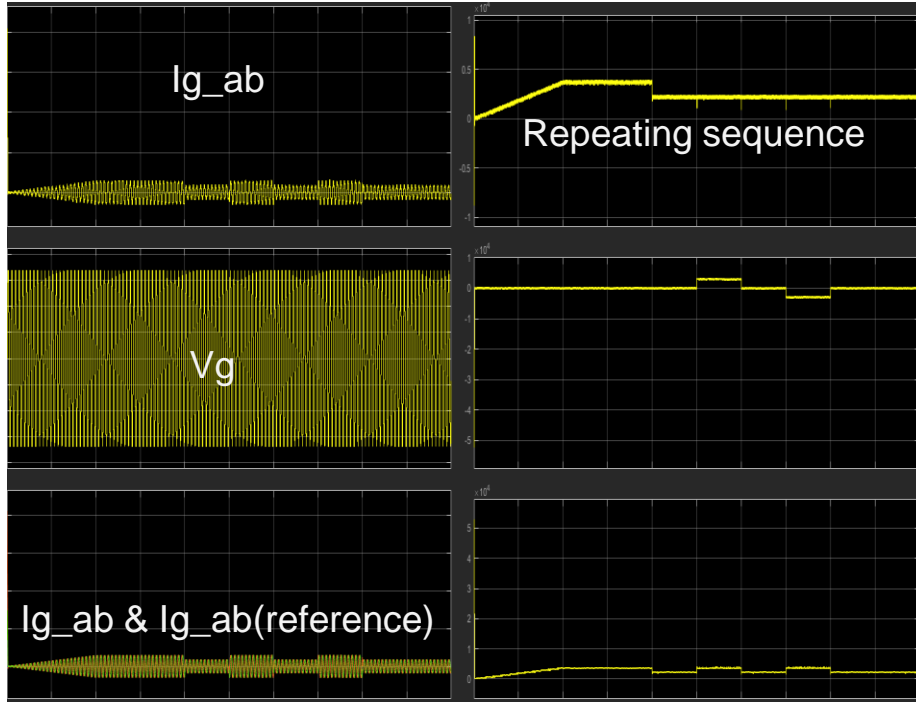
Measurements



[Back](#)

[Display Waveform](#)

Waveform Analysis



After amplification

WBS of Di Miao

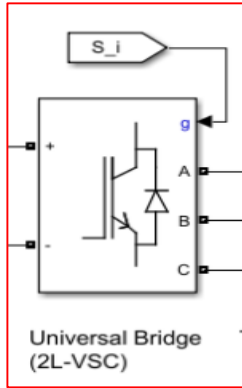
Person Primarily Responsible: Di Miao			
ID	Activity/Task	Description	Deliverable(s)
1.0	simulation		
1.1.1	<i>Design Stationary ($\alpha\beta$) Frame PCC of 2L-VSC</i>	Initialize parameter in Matlab and connect circuit in simulink function	<ol style="list-style-type: none"> 1. Circuit schematic 2. Simulink model 3. waveform
1.1.2	Design Predictive Current Control for Grid-Connected Converter	The PCC scheme for two-level GCC and calculate of reference grid currents	<ol style="list-style-type: none"> 1. Simulink model 2. waveform 3. Feedback signals
2.1	Analyze waveform and results		<ol style="list-style-type: none"> 1. Feedback signals 2. Correct data

← **Completed**

← **Completed**

← **Current Status**

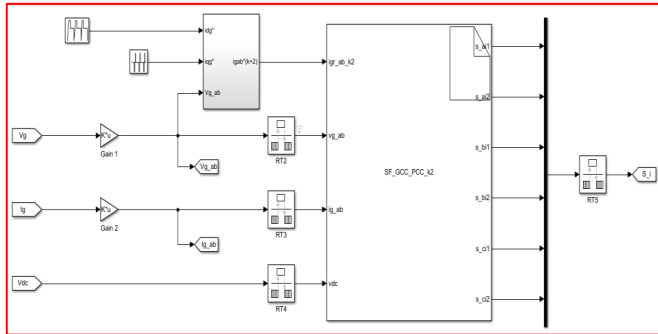
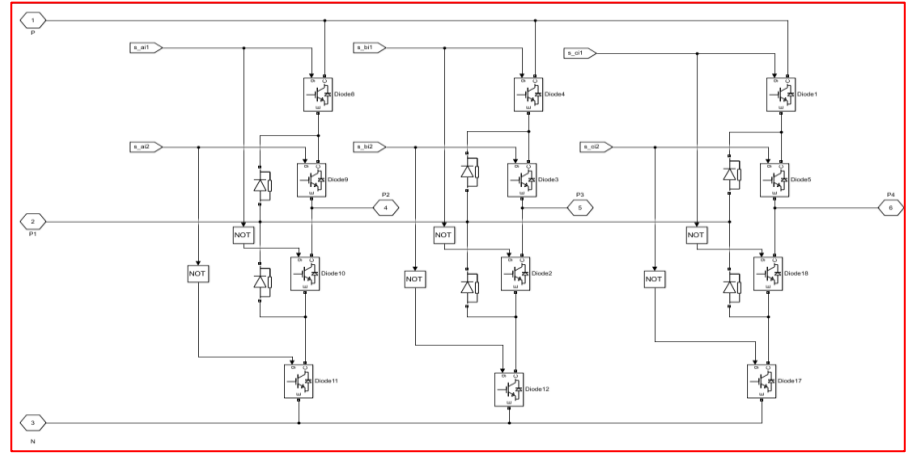
PCC for NPC with grid



2L-VSC



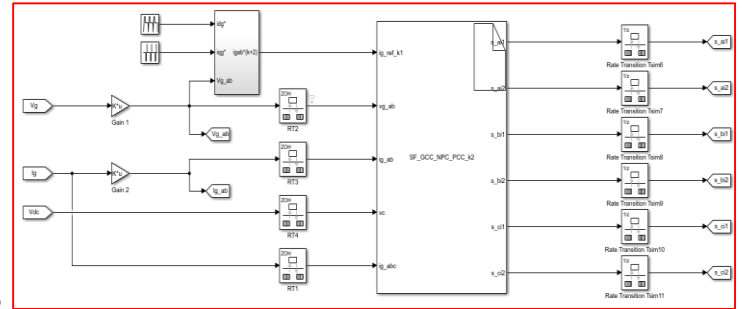
NPC converter



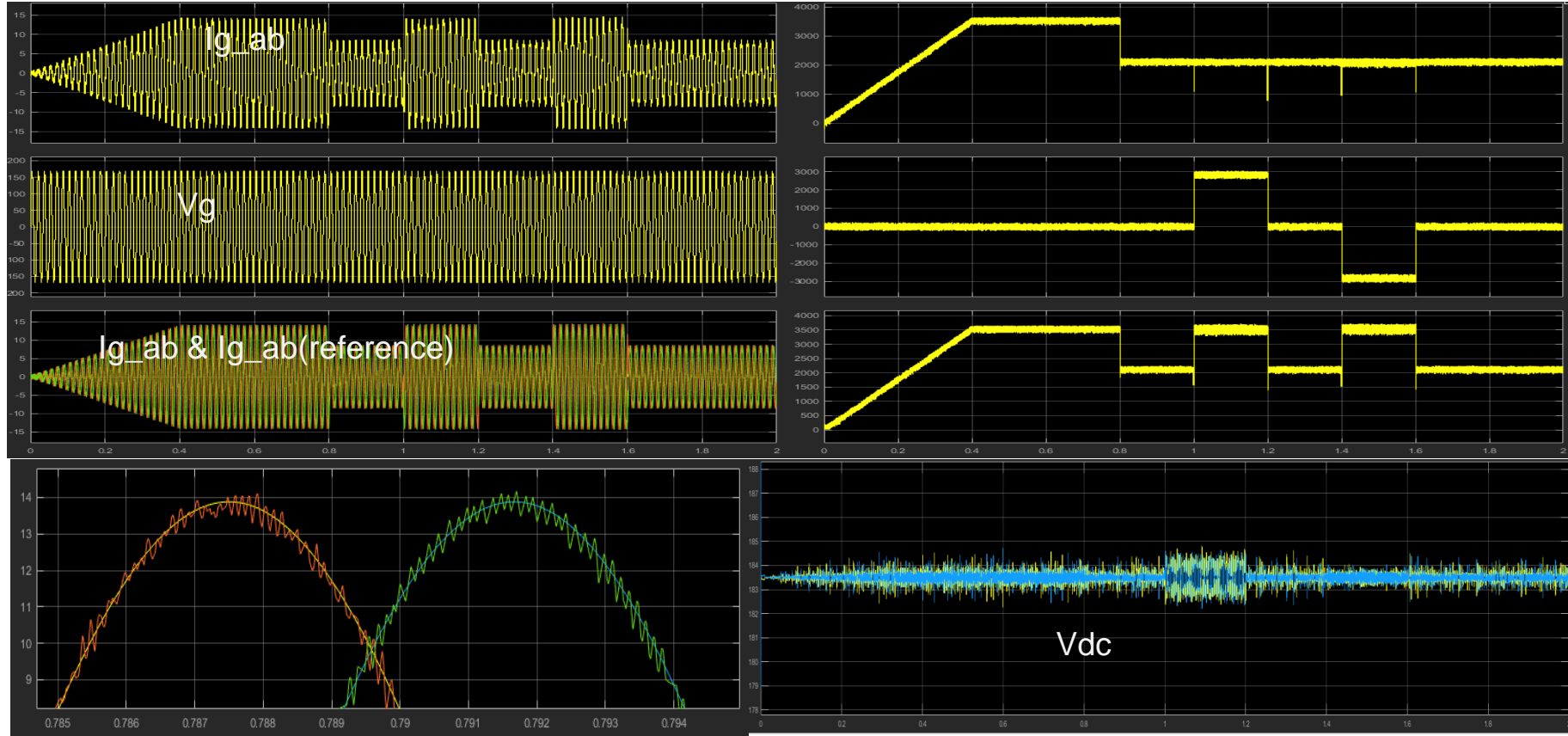
8 Switching states



27 Switching states



Waveform Analysis



WBS of Sayaf

ID	Activity/Task	Description	Deliverables	Other people
1	Hardware			
1.1	Reconnection	Disordered the pins that connect the GD to interface GD boards then connect GD directly to NPC bower board with a wires	Reach 3500w with no shortcut and less noise	Mohammed
1.2	Soldering	Soldering wires instead of 60 bin connectors	Waveform with less noise	Mohammed & Fahad
1.3	Checking before testing	Checking all connections by <u>multimeter</u> to making sure that all connection are right	The NPC converter is working well	Mohammed & Fahad
2	Testing			
2.1	Subsystem testing and set up	Test each PCB's individually to make sure every signals are working then connect all the PCB's together	Build NPC converter	Mohammed & Fahad
2.2	NPC converter testing	Test NPC converter with 350V and 10A	Reach 3500w	Mohammed
3	Writing IEEE Paper			
3.1	Simulation results	Include simulations results from oscilloscopes and explain it	IEEE Paper	Fahad
3.2	Experimental results	Include experimental results and explain it	IEEE Paper	Fahad
3.3	Revise & edit	Revise and edit before submitting the IEEE paper	Revise & edit the IEEE paper	Whole team

Complete

Hardware:

100%

- Reconnection
- Soldering
- Checking before testing

Testing:

60%

- Subsystem testing and set up
- NPC converter testing

Writing IEEE Paper:

0%

- Simulation results
- Experimental results
- Revise & edit

Current status: testing the NPC converter

WBS for Mohammed

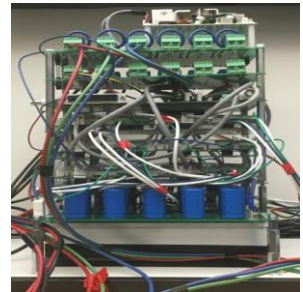
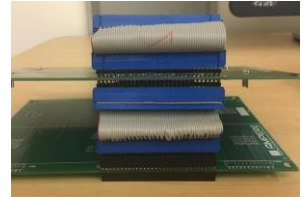
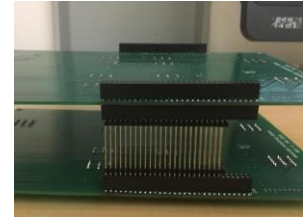
Person Primarily Responsible: Mohammed Abu Radhi.

ID	Activity/Task	Description	Deliverables	Other people
1	Hardware			
1.0	Redesign PCB Boards	Redesign circuits and modify design mistakes.	Verified PCB design.	Sayaf
1.1	Purchase components	Placing order for modified PCB versions, and buying any missing components.	1- Complete list of missing parts. 2- Placing Order for PCB's and other components. 3- Receiving parts.	-
1.2	Soldering	Soldering PCB's, replacing boards' components.	Soldered PCB's.	Hardware Team
1.2.1	Soldering 60 Pin Connectors	Desoldering the old 60 Pin connectors, and replacing them with the new isolated connectors to solve the noise issue.	Proper connection between boards with less noise.	-
2	Testing			
2.0	Subsystems Testing.	Testing each subsystem individually.	Verifying subsystems functionality.	-
2.1	Testing set up	Connecting all PCB's together, and making sure the converter is ready for simulation testing.	NPC converter, and complete testing set up.	Sayaf, and Fahad
2.2	Testing	Doing experiments, and testing the converter.	Converter working properly	Whole Team
3	Writing IEEE Paper			
3.0	Writing the introduction and the conclusion	Each team member is assigned one section to write. My task is to write my assigned section.	Complete Written Sections.	-
3.1	Revise & Edit	Revise and edit the IEEE paper.	Revised IEEE Paper	Whole Team
3.2	Paper Submission	Submitting the IEEE Paper to Dr. Yaramasu.	Submitted paper	-
4	Experimental set up			
4.0	Dismantling testing set up.	Undo hardware testing set up ,and clean lab	Organized lab.	Hardware Team

Activities & Tasks:

1- Hardware (100%) :

- Client required less wiring
- Tried another solutions.
- Noise problem due to small spacing.
- Had to wire them directly.



2- Testing (60%):

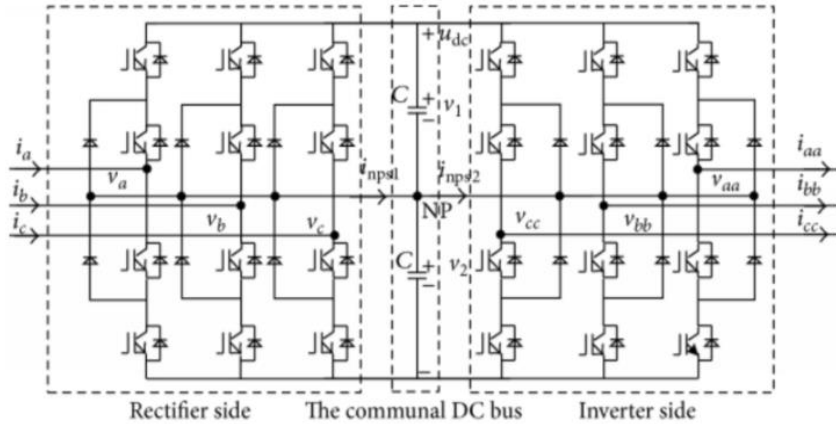
- Successful test for the rectifier side.
- Need to test inverter side.
- Grid connection testing.

3- Writing IEEE Paper :

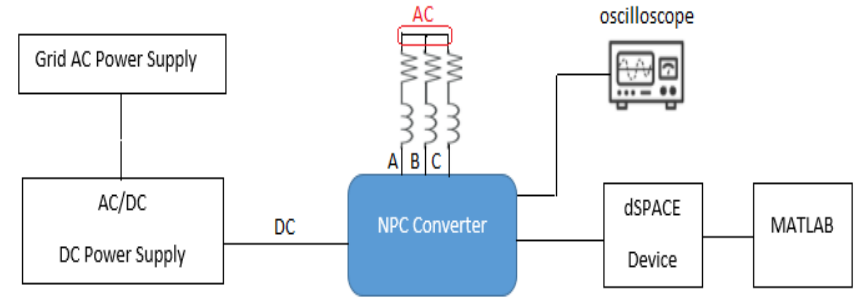
- Writing assigned sections.
- Revision.
- submission.

R Load Test

NPC Converter Schematic

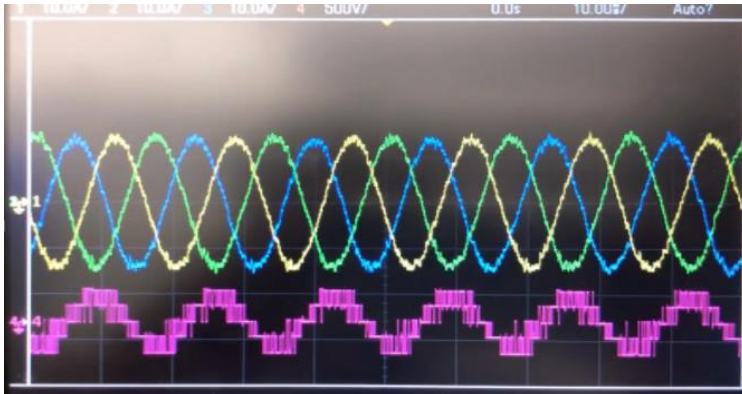


R Load Testing Set Up



Demonstration

Results



- Three phase AC waveforms.
- 5 level line to line AC waveform.

Demo Video



Conclusion

- The team is lagging behind due to the noise problem.
- The hardware section is finally done.
- The team is currently doing experiments and collecting results.
- We will start writing the paper this week.
- Converter met client's requirements and specifications as much as possible.

Thank You

Questions?